Large-Scale 3D Phase Field Dislocation Dynamics Simulations On High-Performance Architectures

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Abstract
In this paper we present the development and performance of a three-dimensional phase field dislocation dynamics (PFDD) model for large-scale dislocation-mediated plastic deformation on high-performance architectures. Through the parallelization of this algorithm, efficient run times can be achieved for large-scale simulations. The algorithm’s performance is analyzed over several computing platforms including Infiniband, GigE, and proprietary (SiCortex) interconnects. Scalability is considered on data sets up to $2,048^3$, along with the efficiency on up to 2,048 processors. Results show that scalability improves as the size of the data set increases and that the overall performance is best on the Infiniband interconnect. In addition, a performance model has been developed to predict run times and efficiency on large sets of data running on multiple processors. This performance analysis shows that this parallel code is capable of harnessing the greater computer power available from petascale systems.

Keywords

1 Introduction
Dislocation dynamics simulations have advanced the understanding of plastic deformation in crystalline materials including several characteristic aspects of plastic deformation such as strength and strain hardening. Plastic deformation of crystalline materials involves complex non-local, non-linear mechanisms with disparate length scales such as long-range dislocation–dislocation interactions, the core structure of the dislocations, and their irreversible interactions with obstacles such as grain boundaries and second phase particles. Owing to these long-range interactions, predictive numerical simulations of plastic deformation in crystalline materials are extremely expensive (Hirth and Lothe, 1968; Bulatov and Cai, 2006; Dimiduk et al., 2006).

Grain refinement in crystalline materials leads to an enhancement of several materials properties including yield and fracture strength and superior wear resistance. These improved material responses benefit the reliability of micro and nano devices, including microelectromechanical systems (MEMS) and nanoelectromechanical systems (NEMS) (Rebeiz, 2003; Walraven, 2003) with components made of nanocrystalline materials. On the other hand, this reduction in grain size is also responsible for new deformation mechanisms that are not present in their coarse-grained counterparts. Some of these mechanisms include size-dependent plastic deformation and plastic strain recovery (Budrovik et al., 2004; Rajagopalan et al., 2007; Uchic et al., 2004).

Numerical simulations should be able to explore these fascinating mechanisms and reveal their key features. One of the challenges in numerical simulations of plastic deformation and one of the reasons these simulations remain limited is that its length and time scales do not lie inside the envelope of traditional multiscale approaches that lead from atomistic processes to a macroscopic response. The characteristic length scales remain in the nanometer range while time scales are in the range of seconds to hours. New models and computer algorithms are necessary to study these complex deformation mechanisms. Therefore, most of the recent efforts in this field are focused on the development of numerical tools to predict plastic deformation in nanocrystalline materials.

Numerical methods describing the evolution of crystalline plasticity can be generally differentiated in two groups, the methods that explicitly track discrete dislocation lines and the methods that follow the evolution of the dislocation density. Discrete dislocations methods explicitly track the
evolution of dislocation lines including their motion, interaction, and multiplication (Kubin and Canova, 1992; Devincre and Roberts, 1996; Zbib et al., 1998; Bulatov et al., 2006). The alternative approach follows the evolution of the dislocation density based on evolution equations derived from dislocation interactions (Groma, 1997; El-Azab, 2000; Zaiser et al., 2001; Limkumnerd and Sethna, 2006). Even though these models do not track individual dislocation lines, the non-local interactions are accounted for and used to derive the evolution equations for the dislocation density.

On the other hand, phase field dislocation models constitute a new approach in which plasticity is described in terms of the strain on each crystallographic slip system, and at the same time may resolve scales such that individual dislocations can be followed during simulations (Finel and Rodney, 2000; Wang et al., 2001; Koslowski et al., 2002). A great advantage of phase field dislocation models is that the evolution of the dislocation fields follows from minimization of the energy of the dislocation ensemble and their interaction with other crystalline defects. Once the mechanisms of interaction of dislocations with defects and their energetics is described, these mechanisms can be integrated into the phase field description.

Some examples of successful integration of these processes are pinning of dislocations at obstacles (Koslowski et al., 2004b, a), interaction of dislocations with interfaces in thin films (Wang et al., 2003; Koslowski, 2007; Hunter and Koslowski, 2008), polycrystals (Wang et al., 2001), dislocation structures in grain boundaries (Koslowski and Ortiz, 2004), and creep (Sullivan et al., 2009). In the present paper we describe an extension of the two-dimensional phase field dislocation model developed by Koslowski et al. (2002) to a three-dimensional phase field dislocation dynamics (PFDD) model with capabilities to represent all 12 slip systems present in face center cubic (fcc) crystals. In addition, the parallelization of this numerical tool and its performance is studied over several architectures including Infiniband, GigE, and SiCortex interconnects.

The following section introduces the PFDD model, including the energetics used to describe the evolution of the dislocations. Section 3 describes the numerical implementation and parallelization of the PFDD model. Section 4 presents results that demonstrate the scalability and performance of the code over different architectures and problems sizes. In addition, Section 4 discusses the impact of different architectures and problem sizes on run times, and shows how this data can be used to predict run times for large simulations on massively parallel computers.

2 3D PFDD

Plastic deformation of crystalline materials is driven by the nucleation and motion of dislocations. Dislocations are line defects present in crystalline materials in high densities \(10^{12} - 10^{17} \text{m}^{-2}\) and are characterized by their slip system given by a slip plane and a slip direction. The PFDD model was developed to predict the evolution of dislocations in fcc materials and, therefore, includes 12 slip systems. In the phase field model, dislocations in each slip system, \(\gamma\), are represented by means of a scalar function, \(\xi^\gamma(x)\). This function is scalar valued and each integer jump represents a dislocation line. This is illustrated in Figure 2, which shows a single dislocation loop expanding and overcoming four surrounding obstacles. Initially, an obstacle will pin the dislocation until a large enough stress builds up allowing the dislocation to overcome and move past the obstacle.

The energy of the system can be written as the sum of two terms Koslowski et al. (2002):

\[
E = E^{el} + E^{int}.
\]

The first term represents elastic dislocation–dislocation interactions and the second describes the interaction of an
where $\sigma_{ij}^{appl}$ is the externally applied stress. Additional terms can be incorporated into the energy equation to account for the core energy of the dislocations, the interaction of dislocations with other defects and the formation of partial dislocations Wang et al. (2001); Koslowski et al. (2002); Shen and Wang (2004). The displacement jump corresponding to one dislocation sliding in the slip plane should be an integer multiple of the Burgers vector. Therefore, the phase field, $\xi(x)$, should take integer values. There are several potential forms satisfy this condition, in the past we have used a piecewise quadratic potential Koslowski et al. (2002). Here we enforce this penalty with a sinusoidal function Wang et al. (2001); Hirth and Lothe (1968).

\[
E^{core} = \sum_{x=1}^{N} A_i \sin^2 (\pi \xi^x(x)) d^3 x,
\] (9)

where $A$ is the unstable stacking fault energy and can be obtained from atomistic simulations.

The time evolution of the dislocation ensemble follows the Ginzburg–Landau equation Wang et al. (2001); Koslowski (2007):

\[
\frac{\partial \xi^x(x,t)}{\partial t} = -\nu \frac{\delta E}{\delta \xi^x(x,t)},
\] (10)

where the kinetic coefficient, $\nu$, determines the velocity of the dislocations and $E = E^{int} + E^{core} + E^{ext}$. Through application of Equations (6)–(9), the term on the right-hand side of Equation (10) can be written as

\[
\frac{\delta E}{\delta \xi^x(x,t)} = \sum_{x'=1}^{N} \frac{\bar{B}_{xx'}(k) \xi^x(x') d^3 k}{(2\pi)^3}
\]

\[
+ \tau^x(x) + \nu A \pi \sin(2\pi \xi^x(x)),
\]

where $\tau^x = \sigma_{ij} s^x m^j$ is the resolved shear stress in the slip plane, $z$.

The PFDD model is used to study the evolution of complex dislocation structures and the resulting macroscopic stress–strain response. As an example, we carry out simulations of dislocations evolving under an external applied shear stress in a confined geometry.

Figure 3 shows the resulting dislocation configurations. The walls perpendicular to the $x$ direction are impenetrable to dislocations and represent the interface in a passivated thin film. Figure 3(a) and (b) show dislocation pile-ups against these impenetrable walls. We consider film thicknesses of 8, 16, and 32 nm and we use Nickel material constants, $b = 0.249$ nm, $\mu = 75$ GPa, $v = 0.2$ and $A = 0.61 J m^{-2}$ (Lee et al., 2010). The computational cells are $32^2$, $64^2$, and $128^2$, respectively. Simulations were performed with the code running on the Steele cluster on up to 64 processors for up to 8,000 time steps at 20 different
applied shear stresses to simulate quasi-static monotonic loading.

From the stress–strain curves we are able to compute yield stress dependency on the thickness. The yield stress is computed using the 0.2% offset yield point. Figure 4 shows the yield stress dependency on the layer thicknesses. As expected the yield stress increases as the thickness decreases following a Hall–Petch relation Hall (1951); Petch (1953):

\[
\sigma_y \sim h^{-0.6},
\]

where \(\sigma_y\) is the yield stress and \(h\) is the film thickness. The exponent is in very good agreement with experimental observation. On the other hand, the values of the yield stress are affected by the initial dislocation density and the microstructure (Hunter and Koslowski, 2010). Parallelization becomes more important for these applications as dislocation density and characteristic size becomes larger and more complex geometries are represented.

3 PFDD Numerical Implementation

This section gives an outline of the parallel algorithm used to implement the PFDD approach presented in the previous section. A flowchart outlining the serial implementation of the PFDD approach is shown in Figure 5.

Initial preprocessing steps include applying loading conditions to the system, setting initial conditions, and calculation of the dislocation–dislocation interaction matrix given by Equation (7). The dislocation–dislocation interaction matrix is one of the most costly calculations completed by the algorithm. However, one advantage is that this matrix is calculated only once during each simulation. This matrix is the size of the grid multiplied by the number of activated slip systems squared. For even a small system of \(32^3\) and 12 slip systems, the dislocation–dislocation interaction matrix will take up 36 MB of memory. As larger and larger simulations are needed, the size and memory required to calculate this matrix quickly grows and the need for parallelization can be seen more directly.

The following step in the algorithm is to solve the Ginzburg–Landau equation, Equation (10). The right-hand side of Equation (10) is computed as an integral in the frequency domain, therefore we calculate the Fourier transform of each phase field using a fast Fourier transform (FFT) algorithm. The FFT is a very effective approach for computing the discrete Fourier transform in \(O(N \log N)\) operations (Cooley and Tukey, 1965). Several iterations are completed until the system deforms in such a way that the internal and external energies are balanced and convergence is reached.

To obtain the discrete Fourier transform of our data we use the multidimensional, complex-to-complex FFTW 2.1.5 subroutine (Frigo and Johnson, 1998). FFTW 2.1.5 is a free, open-source parallel C subroutine library that computes the discrete Fourier transform in one or more dimensions. FFTW uses a 1D decomposition to slice the 3D cube in the \(x\)-direction, so each processor is given a slab/portion of the data. In turn, the rest of the code is parallelized to accommodate this kind of decomposition for consistency. In addition, the FFTW 2.1.5 subroutine allows for data transposition. This can have a significant impact on
the amount of communication needed to complete the Fourier transform. This aspect of the subroutine performance was taken into consideration and analyzed. Further discussion can be found in Section 4.4.1. The majority of the parallelization is achieved using local variables in the loop structures within the algorithm. Consequently, very little MPI communication is added outside what is required for the forward and inverse FFTs, all of which is contained within the FFTW subroutine. Figure 6 shows the 1D decomposition for both normal order and transposed order FFTs.

4 Performance Analysis

The performance of the 3D PFDD model was analyzed on three different interconnects including GigE, Infiniband, and SiCortex. Runs for the GigE and Infiniband interconnects were completed on the Steele Cluster at Purdue University with Dual 2.33 GHz Quad Core Intel E5410 processors with eight cores per node. The SiCortex runs were performed on the Moffett Cluster which is also at Purdue University and has 633 MHz SiCortex 5832 processors with six cores per node. Each run simulated a single dislocation loop expanding on a single slip plane for 100 time steps.

In the following section, results detailing the parallel 3D PFDD model’s performance are presented. This includes investigation of the impact of the interconnection network on the algorithm’s performance, scalability analysis, discussion of the efficiency of the major components of the code, and the MPI overhead measured using the MPI.
Profiler (mpiP) software. In addition, a performance model was applied to the data to predict performance for various cases.

4.1 Impact of the Interconnection Network on Performance

The efficiency of each interconnect is evaluated using two primary criteria: the total run time and scalability over increasing data sizes. Not only is it desired for the code to attain fast run times, but future simulations will require larger data sets and a greater number of time steps in order to analyze more realistic and complex systems. Therefore, the capacity of the parallel implementation to efficiently handle large amounts of computation on large numbers of processors is of great interest. The hardware used to run the code can vastly affect the run time and scalability efficiency. This is obvious from the results displayed in Figure 7(a) and (b), which show run time and scalability data for the different interconnects, respectively.

Simulations completed using the SiCortex interconnect have longer run times in every case. The SiCortex interconnect has a much slower processing time when compared with the Infiniband and GigE architectures, so this result is expected. Conversely, the SiCortex interconnect has fast communication between processors, which results in much better scalability. This is shown in Figure 7(b). This figure compares the speed-up efficiency of the three interconnects over several data sizes. In addition, these results are all compared with an ideal speed-up case, which helps to understand the true efficiency of the parallelization implementation. The SiCortex interconnect shows the best parallelization out of the three interconnects as expected. The Infiniband interconnect also shows moderately good parallelization, with the scalability increasing as the data set size increases. The results from the GigE interconnect show very poor scalability as expected.

After taking into account both run time and scalability, the Infiniband interconnect produced the best results, and the GigE interconnect produced the worst. General trends include better scalability for larger data sets, and lower run times on larger numbers of processors. Both of these trends are desired results, and show that through parallelization of the 3D PFDD model, better computational efficiency can be achieved. Results produced and presented for the remainder of this performance analysis are found using the Infiniband interconnect (unless otherwise stated).

4.1.1 Multidimensional FFT. The 3D PFDD model performs both a forward and inverse multidimensional FFT over all slip systems every time step using the complex-to-complex FFTW function (Frigo and Johnson, 1997). This function accounts for most of the communication time in the code via calls to MPI Alltoall. Therefore, it is important to analyze how the FFTW function behaves within the code.

Figure 7(a) presents run times for various dimension sizes on all three interconnects for three different grid sizes ($64^3$, $128^3$, and $256^3$). Overall, the best runs times came from runs done using the Infiniband interconnect. The GigE runs produced faster run times only on single node runs (using up to eight processors). This may be an indication of more efficient memory bandwidth usage in the GigE interconnect. However, for large data sets, run times on a single node are considerably higher than run times on multiple nodes. This is true in the case of both the Infiniband and SiCortex interconnects. The GigE interconnect does not show a decreasing slope as the number of processors increases. In fact, in all of the runs, this curve tends to level out early on in comparison to the other two interconnects. This is an indication of poor parallelization on this interconnect.

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single node than on multiple nodes. In Figure 8, the slope of the run time curves begins to level off between four and eight cores. This is representative of the bandwidth saturation at four cores.

The forward and inverse FFTW GigE curves exhibit a positive slope going from 8 to 16 cores, showing that communication across nodes via GigE is slower than shared memory communication within node. Between 16 and
128 cores, the slope of the curve remains negative and relatively constant showing that this is the range of good FFTW scalability for the GigE interconnect. Between 128 and 256 cores, the slope of the curve turns positive. On the Infiniband interconnect, the slopes of the forward and inverse FFTW curves sharply decrease starting at eight cores. Unlike the GigE interconnect, communication across nodes via Infiniband interconnect is faster than the shared memory within a node. There is an increase in slope between 128 and 256 processors, although it is not as drastic a change as seen with the GigE network. The network makes such a large impact on the FFTW run times because FFTW is a MPI communication intensive function, and requires a fast interconnect in order to achieve efficient run times. In addition, this can be seen by the considerably faster run times observed on the Infiniband interconnect.

4.2 Results for Weak Scaling

Scalability was discussed above for several different architectures. For a clear picture of how the code scales, its components are analyzed separately, in particular the FFT component which accounts for over 50% of the total run time, and where most of the MPI communication occurs.

This leads to questions about how communication time scales over large data sets and number of processors in comparison with computation time.

A rough idea of the overall scalability of the code may be seen in Figure 9, which shows weak scaling for increasing data size per processor. In weak scaling, the problem size per processor is kept constant as the number of processors increases, while in strong scaling the problem size remains constant as the number of processors is increased. Most of the run time data presented in this paper uses strong scaling unless otherwise stated, as in Figure 9.

In Figure 9, curves showing small data sizes per processor exhibit a positive slope. For data sizes equal to or greater than $2^{18}$, the curves become nearly horizontal, which corresponds to nearly ideal scaling. Thus, the program scales well for large data sizes. This is expected because the computation time in large data sets is far greater than the MPI communication time. For small data sizes, scalability is limited by the MPI communication in the parallel implementation, the majority of which results from the FFTs.

4.3 Scalability of Major Code Components

A run time breakdown of the components within the parallel 3D PFDD algorithm for two large data sets are shown in Figure 10. These timings were observed on the Lawrence Livermore National Laboratory (LLNL) Hera computing cluster, which has an Infiniband interconnect with 16 AMD Opteron quad core processors per node running at 2.3 GHz.

Comparison of the total run time curves in Figure 10(a) and (b) shows that between 256 cores and 512 cores the curve in Figure 10(b) follows closely the ideal curve while the curve in Figure 10(a) deviates from the ideal curve. Thus, the 3D PFDD algorithm is more scalable for the larger data set ($2^{18}$) than for the smaller data set ($1024^3$). The 3D PFDD model is scalable up to $2048^3$ cores for the $2048^3$ size data set, whereas for the $1024^3$ size data set scalability no longer holds at some point between 512 and 1,024 cores.

The components that take the most time include the calls to FFTW and calculation of the dislocation–dislocation interaction matrix. The dislocation–dislocation interaction matrix shows nearly ideal parallelization as the number of processors increases. This is because the subroutine that calculates this matrix requires little to no MPI communication between processors. Since it is entirely based on arithmetic, the parallelization of this portion of the parallel implementation is exceptional.

For small numbers of cores, the interaction matrix calculation is the largest component and decreases linearly in the log–log plot as the number of cores increases. Conversely, the FFTW time is small when the number of cores is small, decreases with increasing processors, and finally turns positive near the maximum number of processors becoming greater than the calculation time for the dislocation–dislocation interaction matrix. This trend is very clear in Figure 10(a). In Figure 10(b), the FFTW time has not increased significantly at 2,048 cores and while the total run time shows poor scalability, it is still decreasing with increasing number of cores.
These results show that not only does the model’s overall scalability improve with increasing data sizes, but benefits can be seen in the individual components of the code. This would lead to the conclusion that the 3D PFDD algorithm will continue to scale well for data larger than $2,048^3$ on more than 2,048 cores. This is important for simulations of more realistic devices and components that may require large, complex data sets.
4.4 MPI Communication

MPI communication was monitored using the computing tool mpiP (Vetter and Chambreau, 2010). mpiP reports the total time the code spent completing all MPI communication commands along with a breakdown of which commands were called and how much time each took to complete. mpiP confirmed that the majority of the communication time is due to Alltoall calls within the FFTW function. From the total run time and the communication time, the arithmetic time can be calculated. Figure 11 shows the total, communication and arithmetic times and scalability can be compared.

Data collected using the mpiP tool shows quantitatively that the communication is the limiting factor of the code’s scalability. The computation time scales linearly as the number of processors increases. Conversely, the MPI communication time decreases only slightly with an increasing number of cores and is lowest on large numbers of processors. However, at the largest number of processors used, there is a sharp increase in the communication run time. As mentioned before, when the data in each processor is made up of only one plane of the 3D grid, the communication between processors outweighs the arithmetic time and causes both the communication and total run times to increase.

4.4.1 Normal Order versus Transposed Order FFT

The FFTW function can return the data in either a normal order (the same as the way it was read into the function) or a transposed order form. The transposed order requires half as many MPI Alltoall calls, which cuts the communication time in half. However, the transpose option requires modification of the code, specifically in the way the dislocation-dislocation interaction matrix is created. The run time difference between normal order and transposed order FFTW calls are compared in Figure 12.

Figure 12 compares times from three components of the code: the total 3D PFDD run time, the time taken to complete the time evolution loop, and the MPI communication time. The MPI communication time for the transpose order FFTW is half that of the normal order FFTW resulting in a smaller time evolution loop run time for the transpose order. However, the difference between the transpose and normal order total run times of the PFDD algorithm remains small. The difference in the MPI communication time is offset by the time taken for preprocessing outside of the time evolution loop, in particular the calculation of the dislocation–dislocation interaction matrix. Although this matrix requires little to no MPI communication, its large size requires a great amount of computation time relative to MPI communication time. As the number of cores increases to 512, the computation time per node decreases while latency increases and scalability is no longer valid.

It must be noted that these runs were completed for 100 time steps. Since the majority of MPI communication is contained within the FFTW subroutine which is called twice every time step, it is reasonable to expect that the amount of MPI communication will increase with increasing time steps. In addition, the dislocation–dislocation interaction matrix is calculated once at the beginning of the code and is independent of the number of time steps. As the number of time steps grows, the computation time for the dislocation–dislocation interaction matrix will remain the same while the MPI communication time grows. At large numbers of time steps, the MPI communication will dominate over this computation time and the reduction in communication time from the transposed order FFTW will be reflected in the overall run time as well as in the time evolution loop. This could be important for certain applications of the PFDD approach, such as in comparison with molecular dynamic simulations, which require large numbers of time steps for convergence.

4.5 Performance Model

All of the different factors that influence the PFDD algorithm’s run time can be described by an equation of the form (Dmitruk et al., 2001)

$$T = t_c \frac{N^3 \log_2(N^3)}{P} + t_eN^3 + t_sN^3 + t_w2P, \quad (13)$$

where $N$ represents the grid size in one dimension, $P$ is the number of processors, $t_c$ represents the FFT computation.
time per word on a single processor, \( t_0 \) is the time it takes for a memory-to-memory copy of a word, \( t_w \) is the time it takes for a single word to transmit between nodes, and \( t_r \) represents the start-up or latency time for a message.

The run time of a code is composed of two major components: computation time and communication time. The first term in Equation (13) corresponds to the time needed to compute the FFTs, and the second term reflects the time needed to rearrange data. These terms both correlate to the computation time of the code. The third term accounts for the time it takes to send and receive information between nodes, and the fourth term represents the latency time for a message. These last two terms represent the MPI communication time.

As the number of processors, \( P \), increases, the first three terms of Equation (13) decrease at a rate inversely proportional to \( P \), while the fourth term, the latency component, increases proportionally to \( P \). For large numbers of processors, the latency component becomes larger than the sum of the first three terms, and the total run time increases. This explains the sharp increase in slope in run time curves shown in Figure 10. This is generally seen when the number of processors is at a maximum and each processor has only one slab of the 3D grid, which results in a great increase in communication between processors. This effect is reflected in the latency term in Equation (13). In addition, since the latency term correlates to MPI communication, the increase in slope with the large number of processors can be seen in FFTW run times since this function is communication intensive (see Figures 8, 11, and 12). However, the slope increase will not be seen in computation-based components, such as calculation of the dislocation–dislocation interaction matrix (see Figures 10 and 11).

Using data obtained from the runs presented above, a predictive model can be formulated to determine how long larger runs will take, and how many processors are needed to maximize efficiency. Results from runs completed on the Infiniband interconnect at Purdue University along with least squares regression were used to compute the parameters in

\[
T(N, P) = 8.92 \times 10^{-7} \frac{N^3 \log_2 N^3}{P} + 2.92 \times 10^{-4} \frac{N^3}{P} - 2.64 \times 10^{-4} P
\]

which is based on Equation (13). Equation (14) was used to predict run times for the 3D PFDD with large data sets. These predictions were then compared to runs completed on LLNL Hera computing cluster. Figure 13 shows the actual run times from Purdue’s Steele cluster and from LLNL’s Hera cluster as well as predicted times calculated from Equation (14). The predictions provide a good estimation of total run times.

5 Conclusions

We have presented a 3D phase field dislocation dynamics model that enables efficient simulations of plastic deformation in crystalline materials. The code is implemented in parallel and makes use of the FFTW library, a multidimensional FFT (Frigo and Johnson, 1997). In addition, the performance analysis shows that this parallel algorithm is portable across different architectures.

We have performed numerical simulations on up to 2,048 cores and the algorithm shows high computational efficiency and scalability in large data sets. The performance model developed allows us to estimate with very good accuracy run times for large data sets as well as the number of processors needed to achieve high efficiency. This new algorithm will increase the capacity to accomplish more advanced simulations of plastic deformation with more realistic microstructures in domain sizes not attainable with serial dislocation dynamics implementations. This will greatly aid the study of intricate dislocation structures and their interactions with grain boundaries, voids, and interfaces.

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References


**Bios**

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