

---

# KOFO ADAFIN

2807 S Beck Lane Apt. 6 Lafayette IN, 47909 • (507) 261-0403 • kadafin@purdue.edu

---

## CAREER PROFILE

- Able to leverage over four years of professional experience in hardware and software engineering, and network administration.

## EDUCATION

### KRANNERT SCHOOL OF MANAGEMENT

PURDUE UNIVERSITY

Master of Business Administration (MBA Candidate 2013)

- **Graduate Assistant**, Burton D. Morgan Center for Entrepreneurship

West-Lafayette, IN

2011-2013

### PURDUE UNIVERSITY

School of Electrical and Computer Engineering

Bachelor of Science, Computer Engineering (BSCmpE) with minor in Economics

- Established my own Consulting Start-Up and worked at IBM and Delphi Labs to finance my education.
- **President**, Alpha Phi Alpha Fraternity Inc.

West-Lafayette, IN

2003 -2009

## PROFESSIONAL EXPERIENCE

### WYDE CORPORATION

**Software Developer and Reinsurance Consultant**

- Developed and tested custom reinsurance solutions for the Colorado Bankers Life (CBL) Wynsure insurance policy administration system.
- **Project Lead**, Became CBL reinsurance module main contact and project lead.
- Supported client system testers by answering configuration and processing questions.
- Performed knowledge transfer of reinsurance process to CBL developers.

Eagan, MN

2010-2011

### ADAFIN CONSULTING

Technology Consulting Start-Up

**Founder and President**

- Performed numerous computer hardware installations/repairs as well as diagnostic tests and virus removals.
- Managed numerous Microsoft Windows Server environments with Active Directory infrastructure.
- Built and maintained LAMP (Linux, Apache, MYSQL, Perl/Python/PHP) Servers.
- Produced custom database reporting scripts to improve user productivity and alert them of pending tasks.

West-Lafayette, IN

2006-Present

### INTERNATIONAL BUSINESS MACHINES (IBM)

Systems & Technology Group Co-op

- Tested usage of SDC converted Einstimer assertions files with Synopsys and Cadence synthesis tools.
- Built numerous functions in C to improve usability and functionality of IBM's EDA tools suite.
- Converted tcl and tk scripts into Perl, Python, and C++ and **decreased script runtime by 98%**.
- Created Perl scripts to automate compiling, and comparing digital asset repositories.
- Enhanced Placement Driven Synthesis (PDS) plot tools to support hierarchy layers.
- Created Verilog test benches to test VHDL behavior models of mitigated ASIC cores.
- Created a spreadsheet that calculates the power used by different facets of an ASIC chip.

Rochester, MN

2005-2008

## LEADERSHIP AND HONORS

- Management Leadership for Tomorrow (MLT), MBA Preparation Program Fellow, 2011
- Whirlpool Leadership Development Institute Participant, 2011
- Volunteer Big Brother, Big Brothers Big Sisters, 2009
- Recipient of Purdue Black Caucus Academic Award, 2005
- Saxophonist, Purdue Jazz Band combo group, 2005
- Volunteer Mentor, First Tee, 2004